

Abstract

An embodiment of a current selective D flip-flop circuit comprises a D flip-flop, a current selector and a current multiplier is disclosed. The current selector is used for receiving and summing at least two currents to form a summed current and having a current comparator for comparing the summed current with one of the at least two currents and selecting one of the at least two currents as an output current. The output current is steered through the current multiplier for biasing the D flip-flop.